FIG. 1

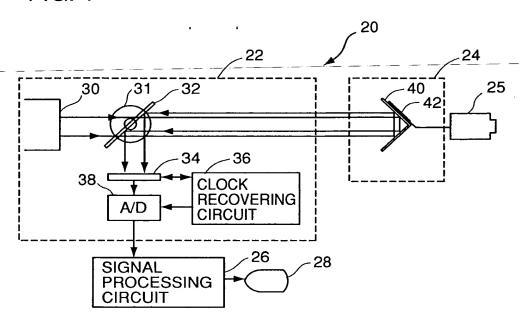


FIG. 2

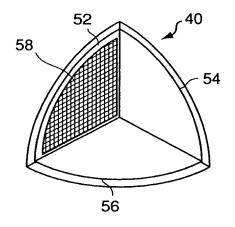


FIG. 3 20 24 22 40 25 30 31 -32 -42 **—36** 38.5 CLOCK RECOVERING CIRCUIT A/D - 26 SIGNAL PROCESSING CIRCUIT -28

FIG. 4

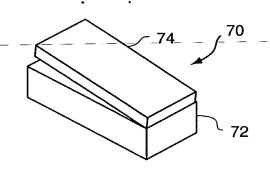


FIG. 5

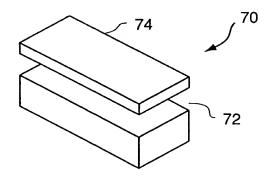


FIG. 6

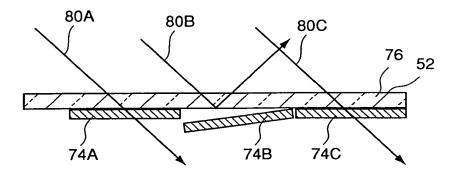


FIG. 7

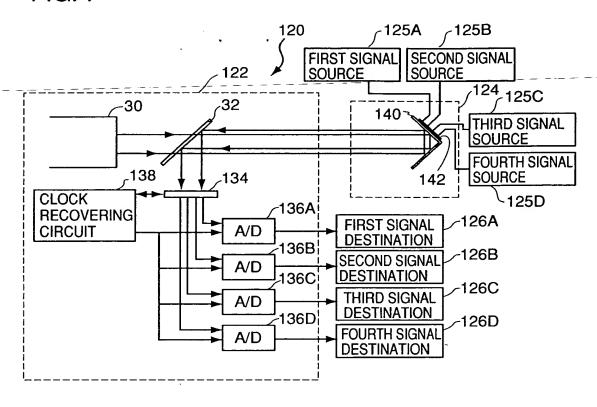


FIG. 8

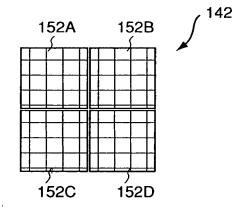
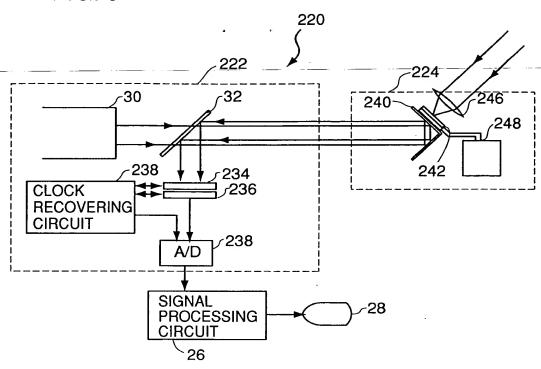


FIG. 9



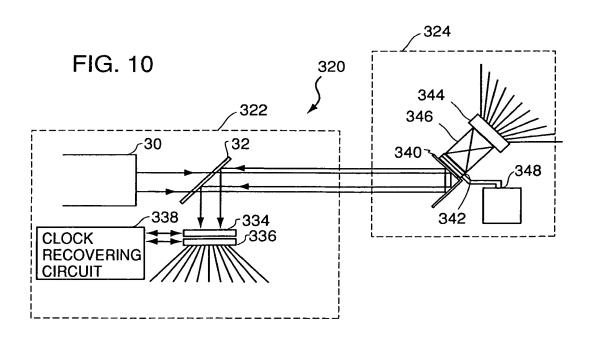


FIG. 11

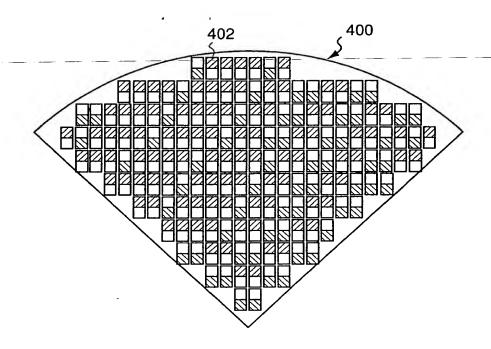


FIG. 12

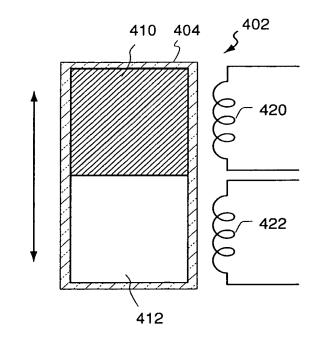
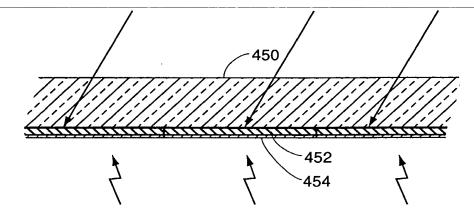


FIG. 13



440

FIG. 14

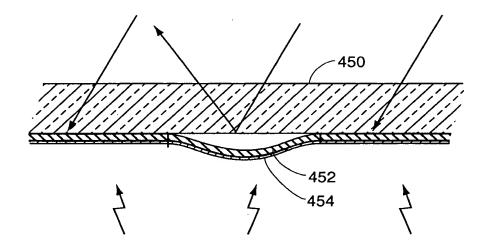


FIG. 15

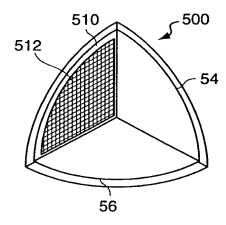


FIG. 16

